

IFD

Attorney's Docket No. 42P17814
Confirmation No.: 8503

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Robert Chau, et al.

Application No.: 10/750,028

Filed: January 16, 2004

For: TRI-GATE TRANSISTORS AND
METHODS TO FABRICATE SAME

Examiner: Smith, Bradley

Art Unit: 2891

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: April 4, 2006


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)



Complete if Known

Application Number	10/750,028
Filing Date	January 16, 2004
First Named Inventor:	Robert Chau, et al.
Art Unit	2891
Examiner Name	Smith, Bradley
Attorney Docket No.:	42P17814

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
		US-	2003/0122186 A1	07-03-2003	Sekigawa et al.	
		US-	2003/0151077 A1	08-14-2003	Mathew et al.	
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		US-	2004/0238887	12-02-2004	Nihey	
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Signature

Date Considered

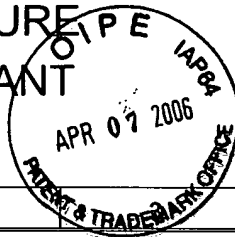
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		WO 2004/059726 A1	07-15-2004	IBM Corporation		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		International Search Report PCT/US2005/033439, mailed 1/31/2006 (7 pgs.)	
		International Search Report PCT/US2005/035380, mailed 2/13/2006 (14 pgs.)	
		International Search Report PCT/US2005/037169, mailed 2/23/2006 (11 pgs.)	
		SUNG MIN KIM, et al., "A Novel Multi-channel Field Effect Transistr (McFET) on Bulk Si for High Performance Sub-80nm Application, IEDM 04-639, 2004 IEEE, pgs. 27.4.1-27.4.4	
		YANG-KYU CHOI, et al., "A Spacer Patterning Technology for Nanoscale CMOS" IEEE Transactions on Electron Devices, Vol. 49, No. 3, March 2002, pgs. 436-441	
		W. XIONG, et al., "Corner Effect in Multiple-Gate SOI MOSFETs" 2003 IEEE, pgs. 111-113	
Examiner Signature		Date Considered	

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